



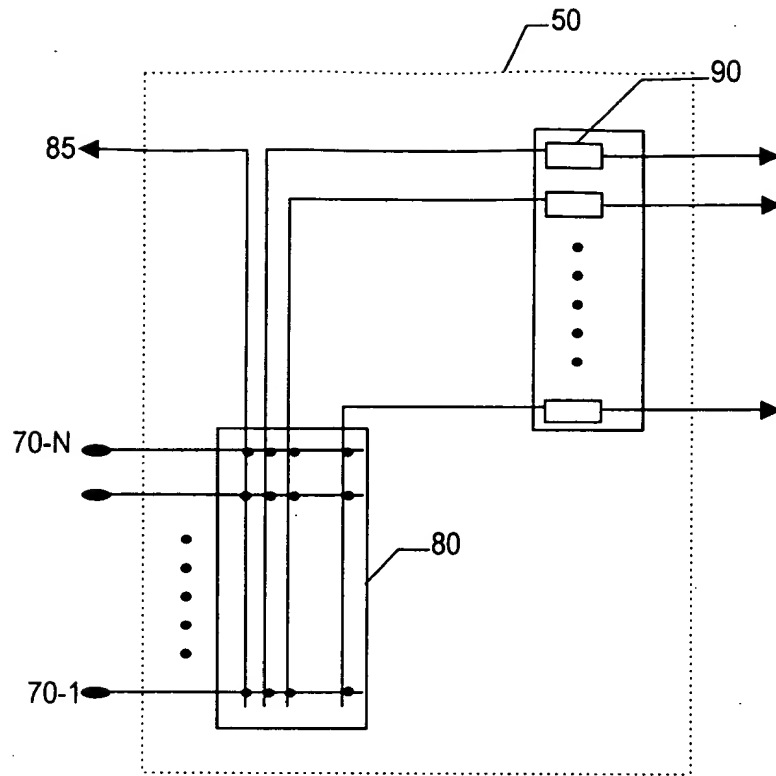
[illegible]

FIG. 3

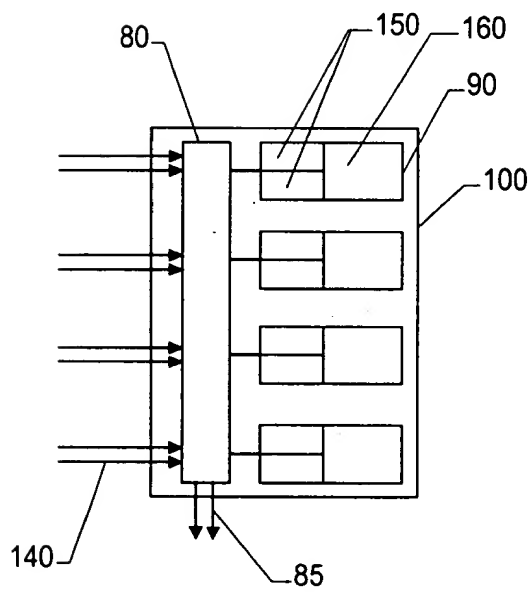


FIG. 4

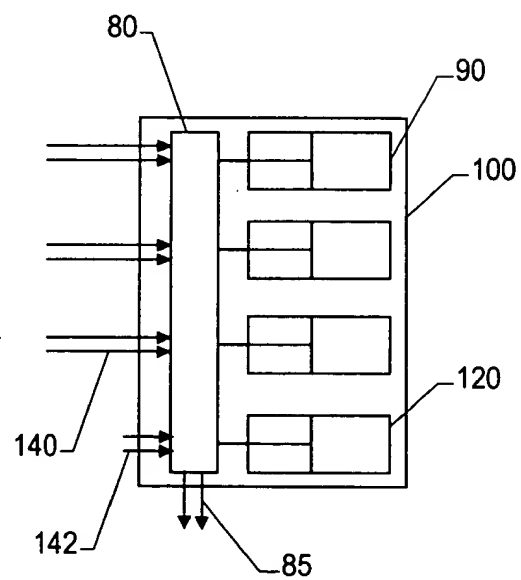


FIG. 5

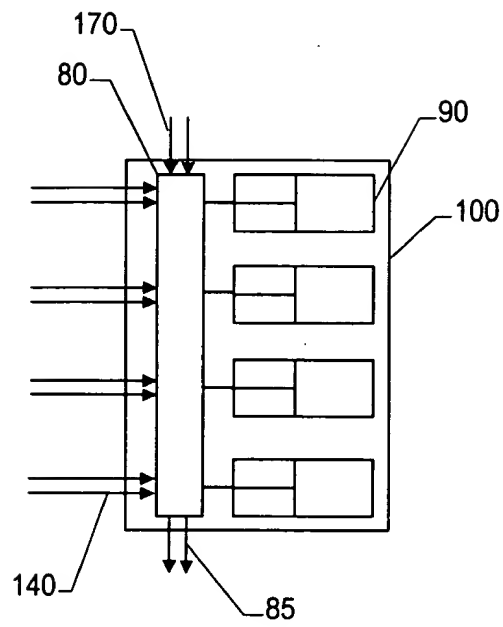


FIG. 7

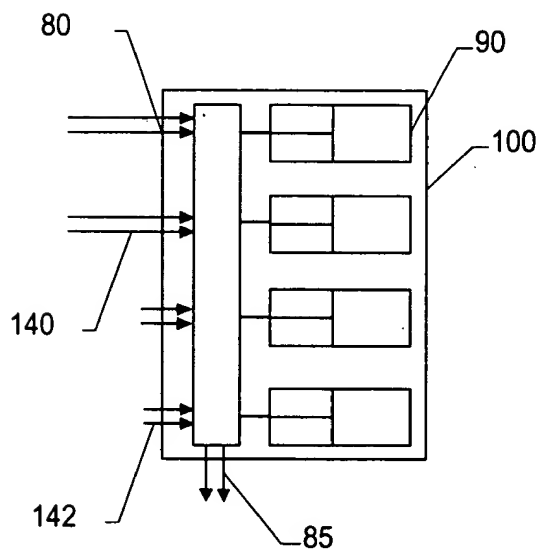


FIG. 9

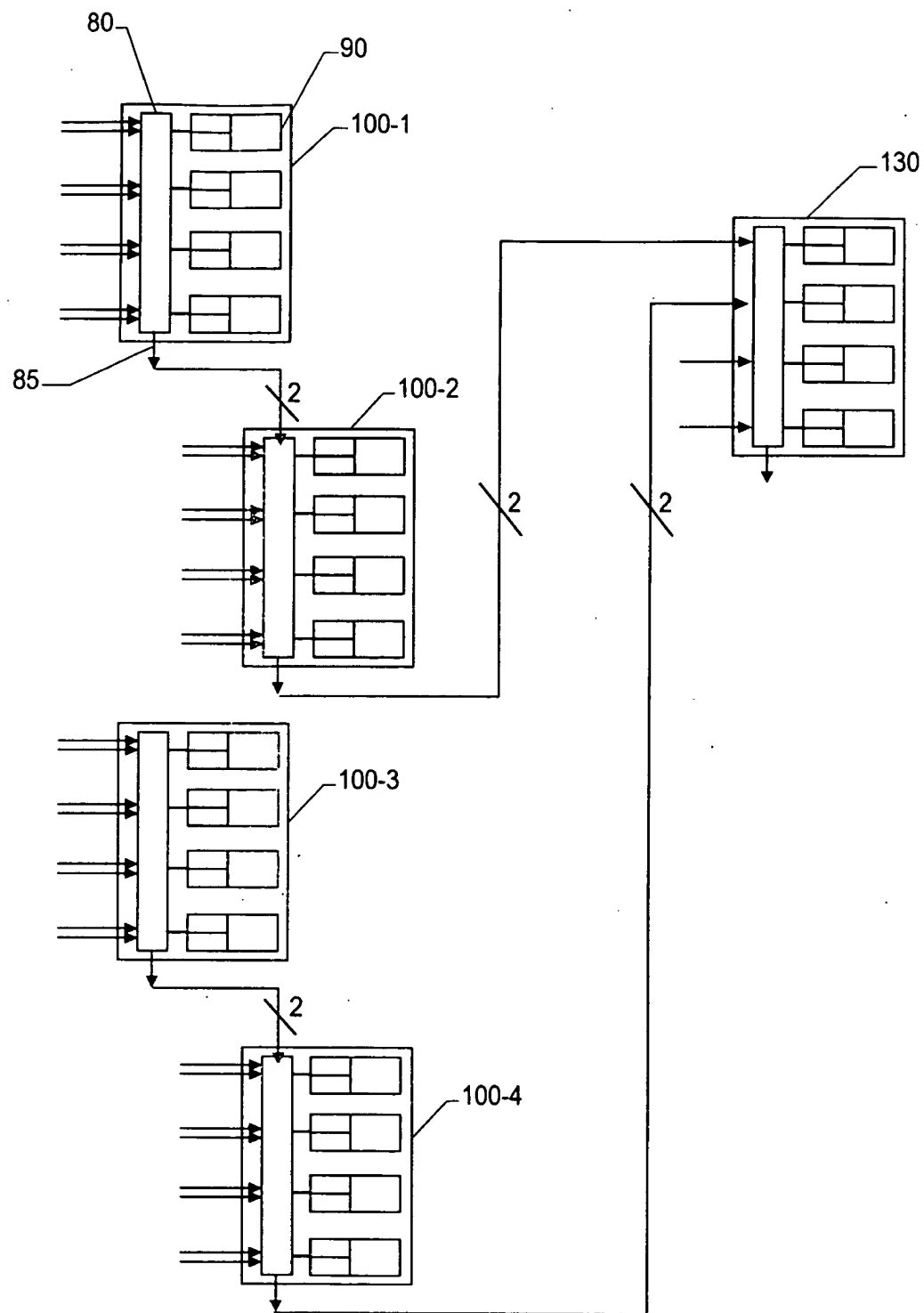


FIG. 8

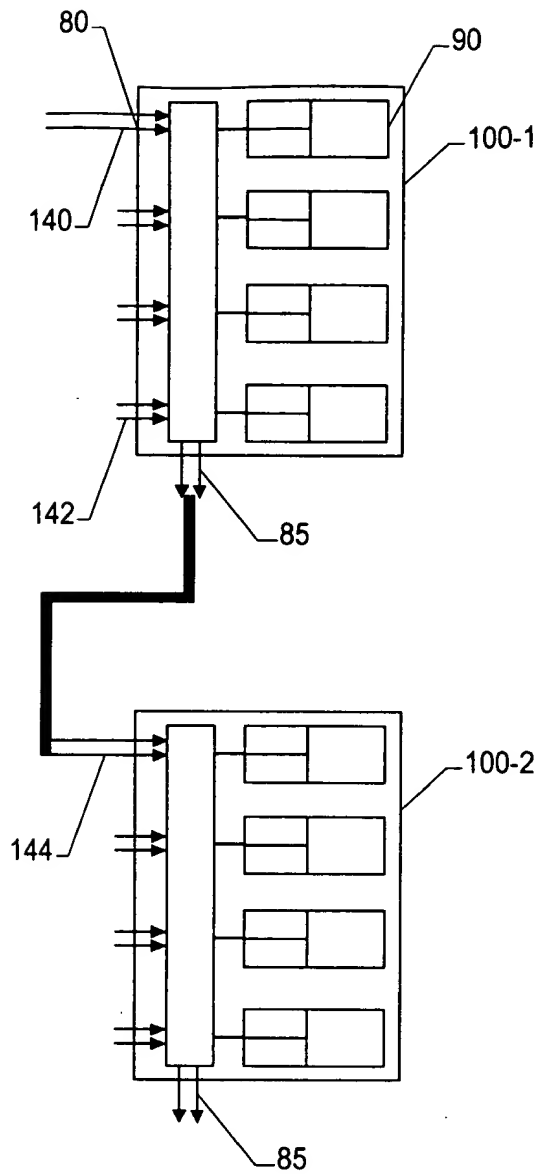


FIG. 10

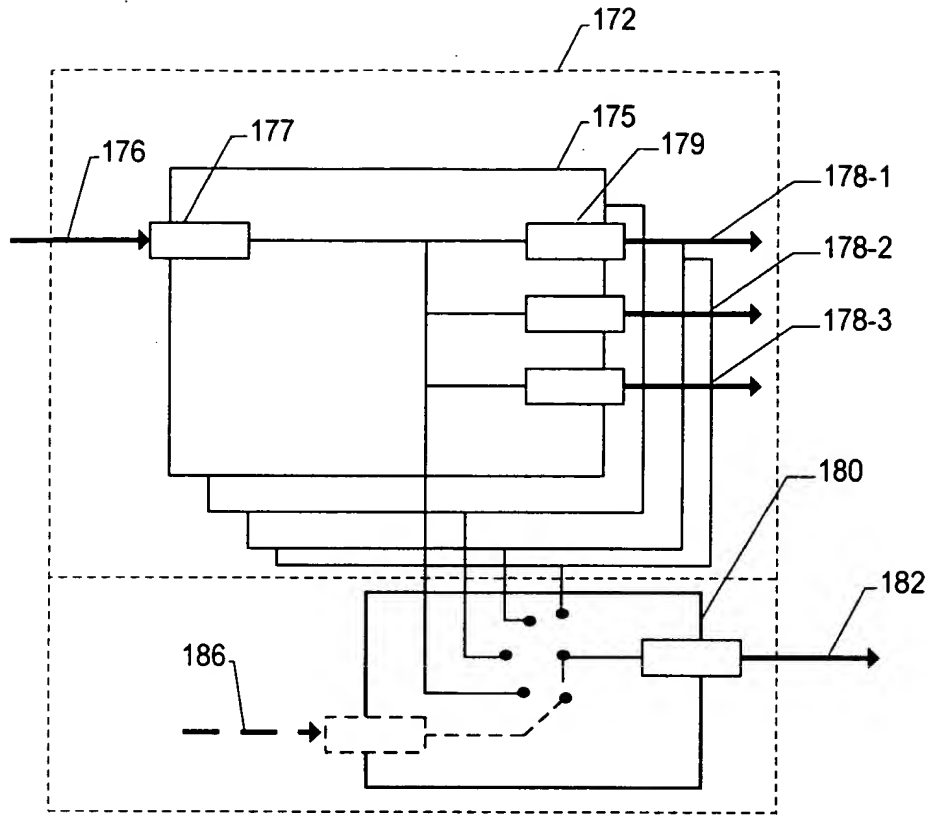


FIG. 11

Figure 1 is a block diagram of a system architecture. On the left, a vertical bus structure is shown with two lines, L1 and L2, indicated by arrows. Two identical processing blocks are connected to this bus. Each block has an input 176, a control input 172, and an output 178. The output 178 of each block is connected to a set of four parallel lines, each labeled 4. These lines connect to two output modules, 100-1 and 100-2. Each output module has a control input 130 and a data input 184. The output of each module is a signal labeled ≤ 6 .

FIG. 12

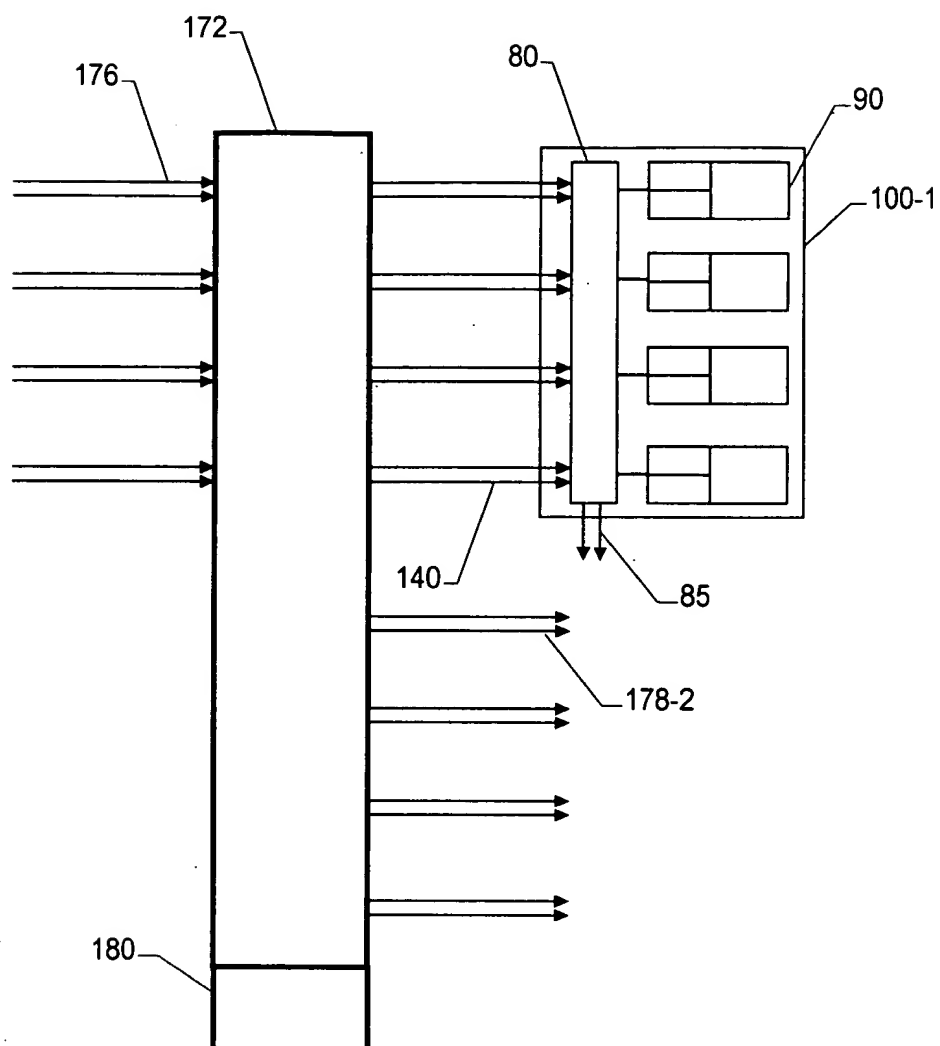
[illegible]

FIG. 13A

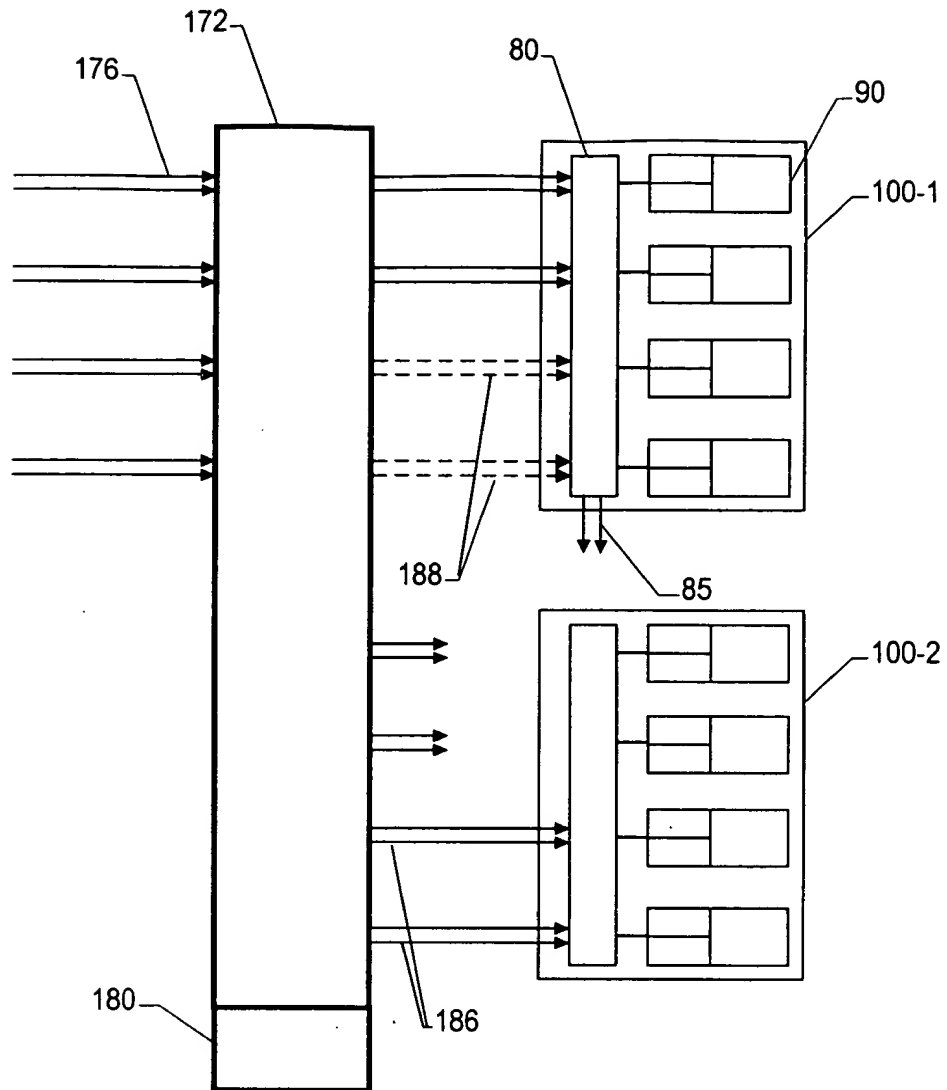


FIG. 13B

The diagram illustrates a network system architecture. On the left, a vertical block labeled 'NET I/F' (200) is connected to four external network pairs: 'EXT 1,2', 'EXT 3,4', 'EXT 5,6', and 'EXT 7,8'. A reference numeral '85' points to the top of this block. To its right is a vertical block labeled 'SWITCH' (80). Bidirectional arrows connect 'NET I/F' to 'SWITCH' for channels 'CH 1,2', 'CH 3,4', 'CH 5,6', and 'CH 7,8'. A reference numeral '90' is placed above the switch section. The 'SWITCH' is connected to a 'LAN' block (100) on the right. The 'LAN' block is further connected to 'LAN A' and 'LAN B'. A reference numeral '230' points to the bottom of the 'LAN' block. At the bottom, a horizontal block labeled 'MANAGEMENT' (235) is connected via bidirectional arrows to the 'NET I/F', 'SWITCH', and 'LAN' blocks. A reference numeral '235' also points to the 'MANAGEMENT' block. A bracket labeled '90' spans the top of the switch and LAN sections. A reference numeral '150' points to the top of the first switch-to-LAN connection, and '160' points to the top of the second switch-to-LAN connection.

FIG. 14

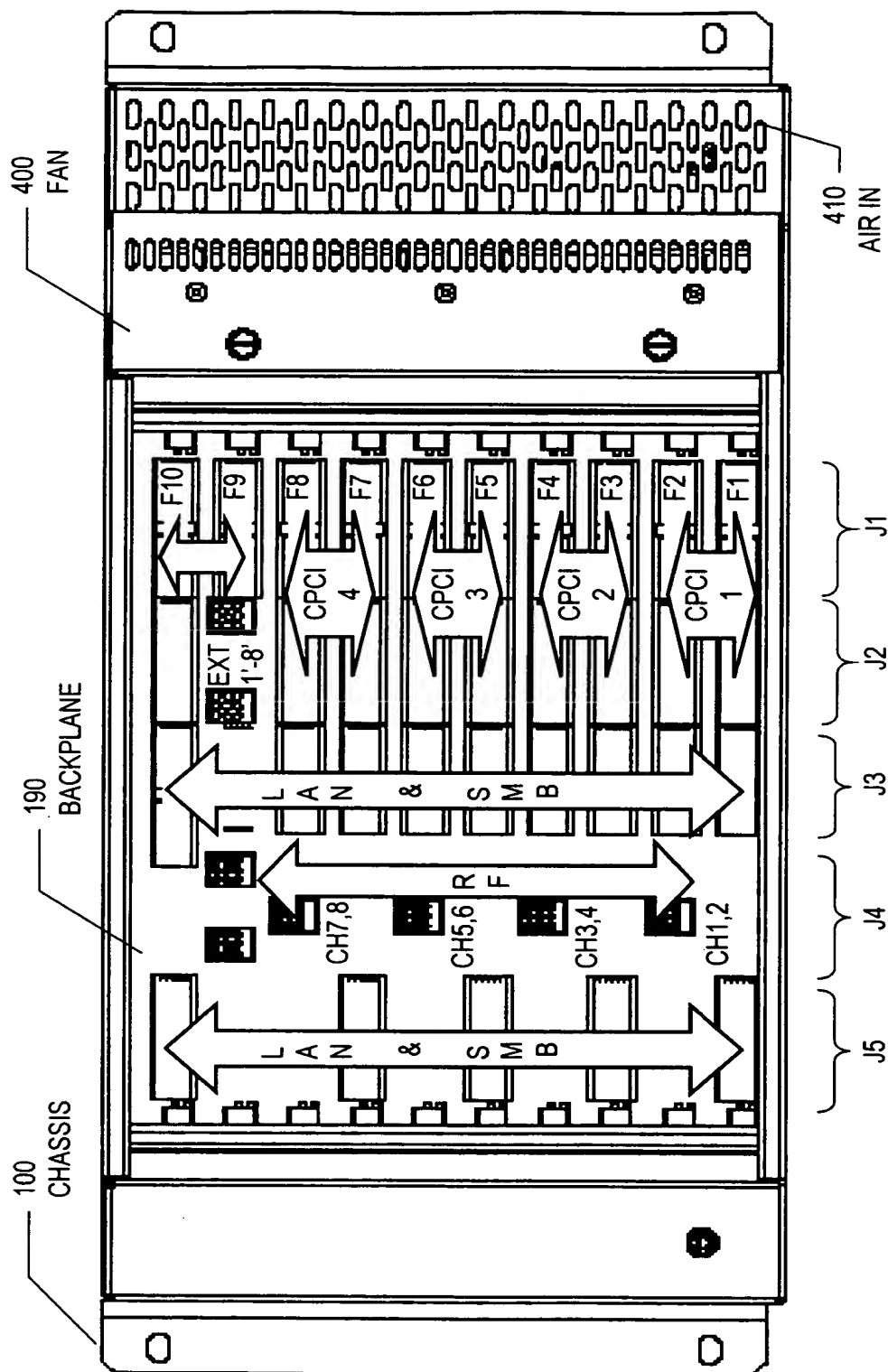


FIG. 15B (FRONT)

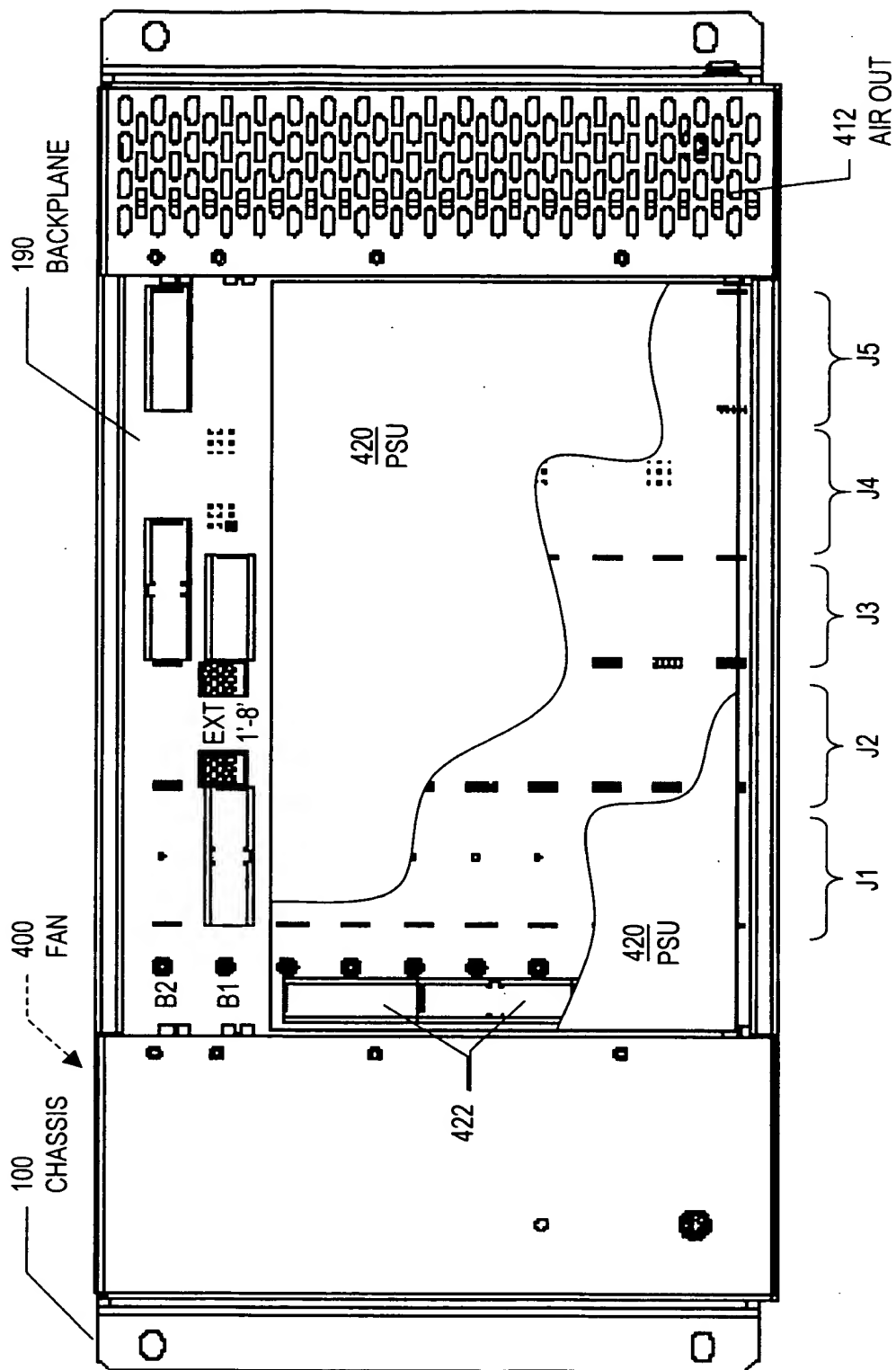


FIG. 15C (REAR)

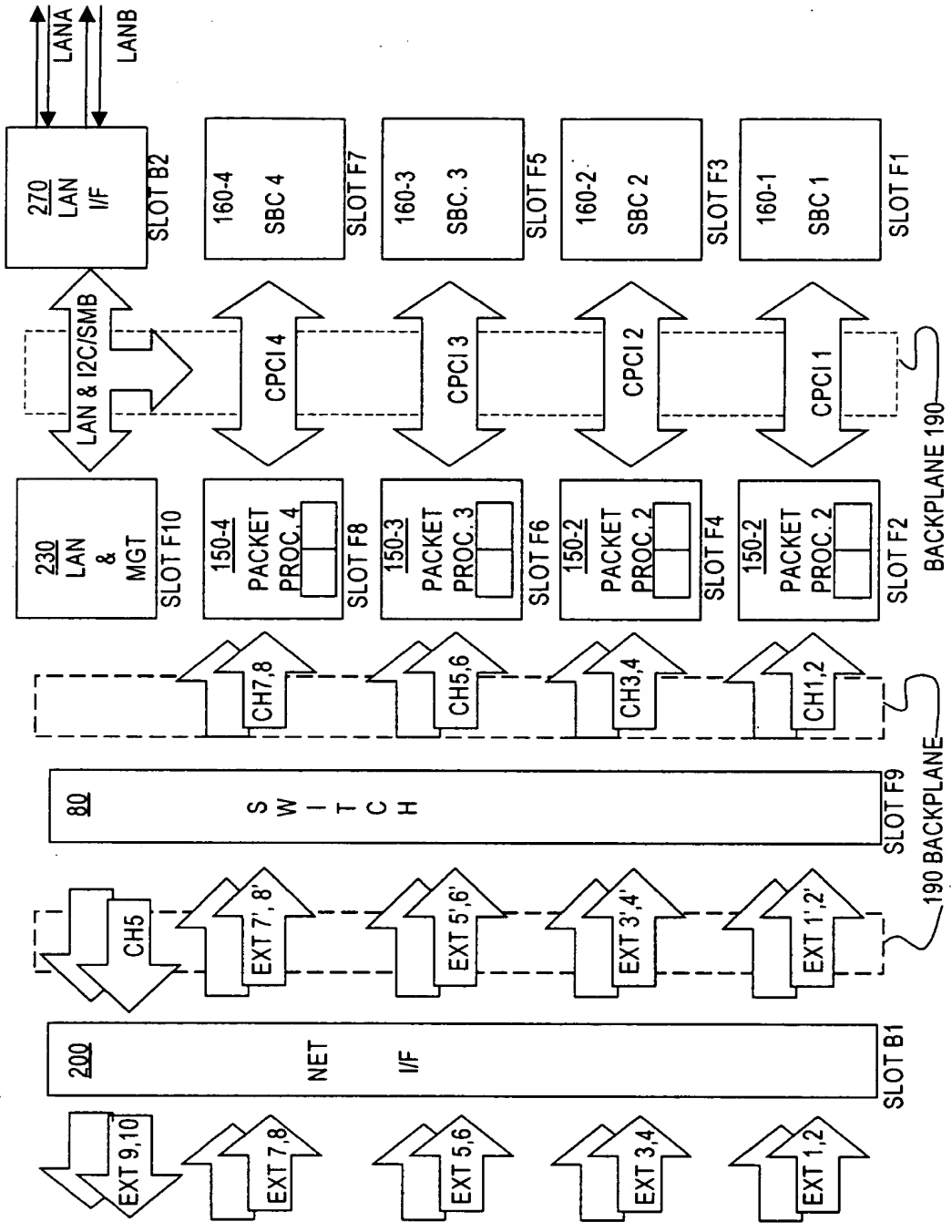


FIG. 16

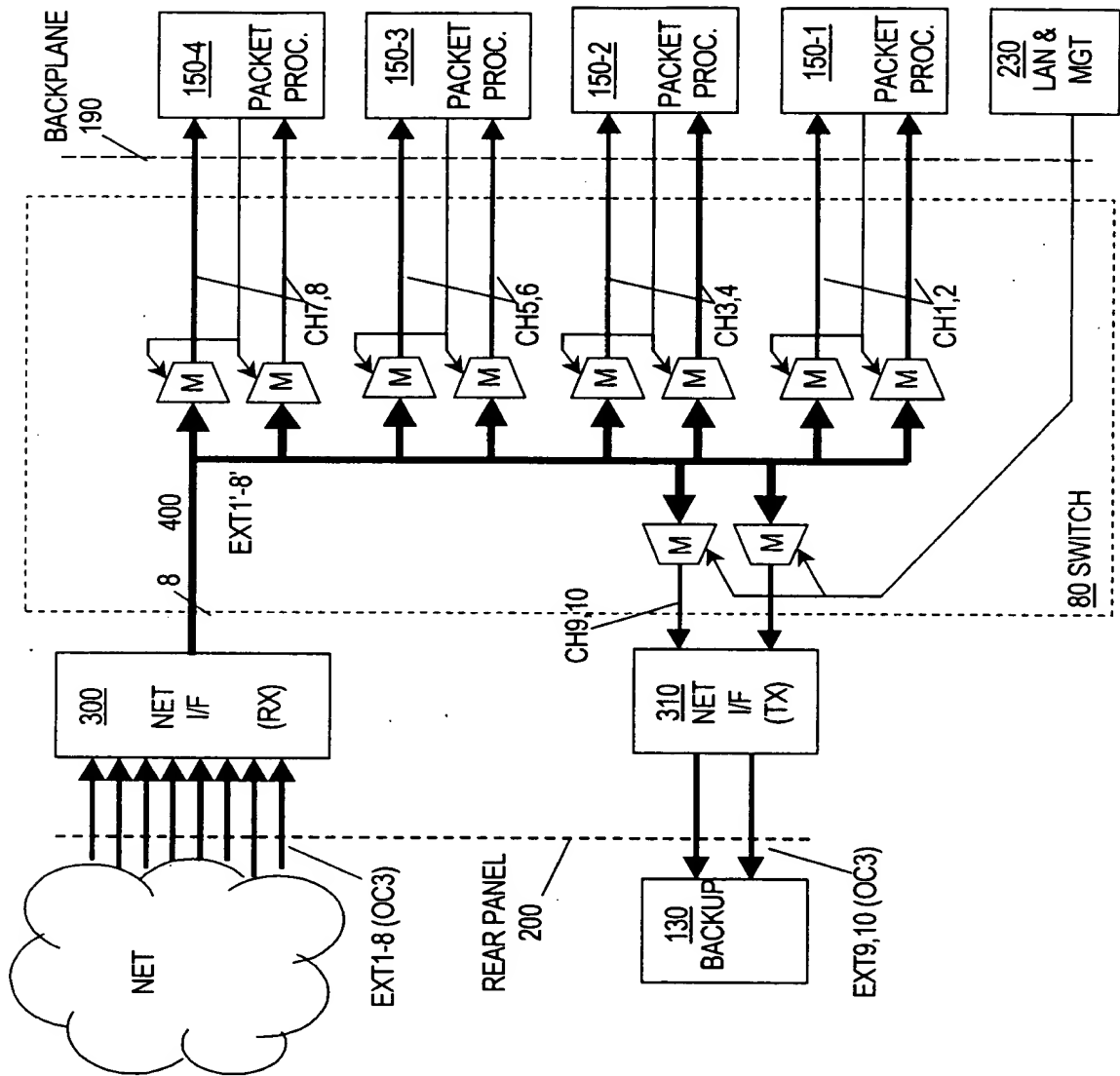


FIG.17

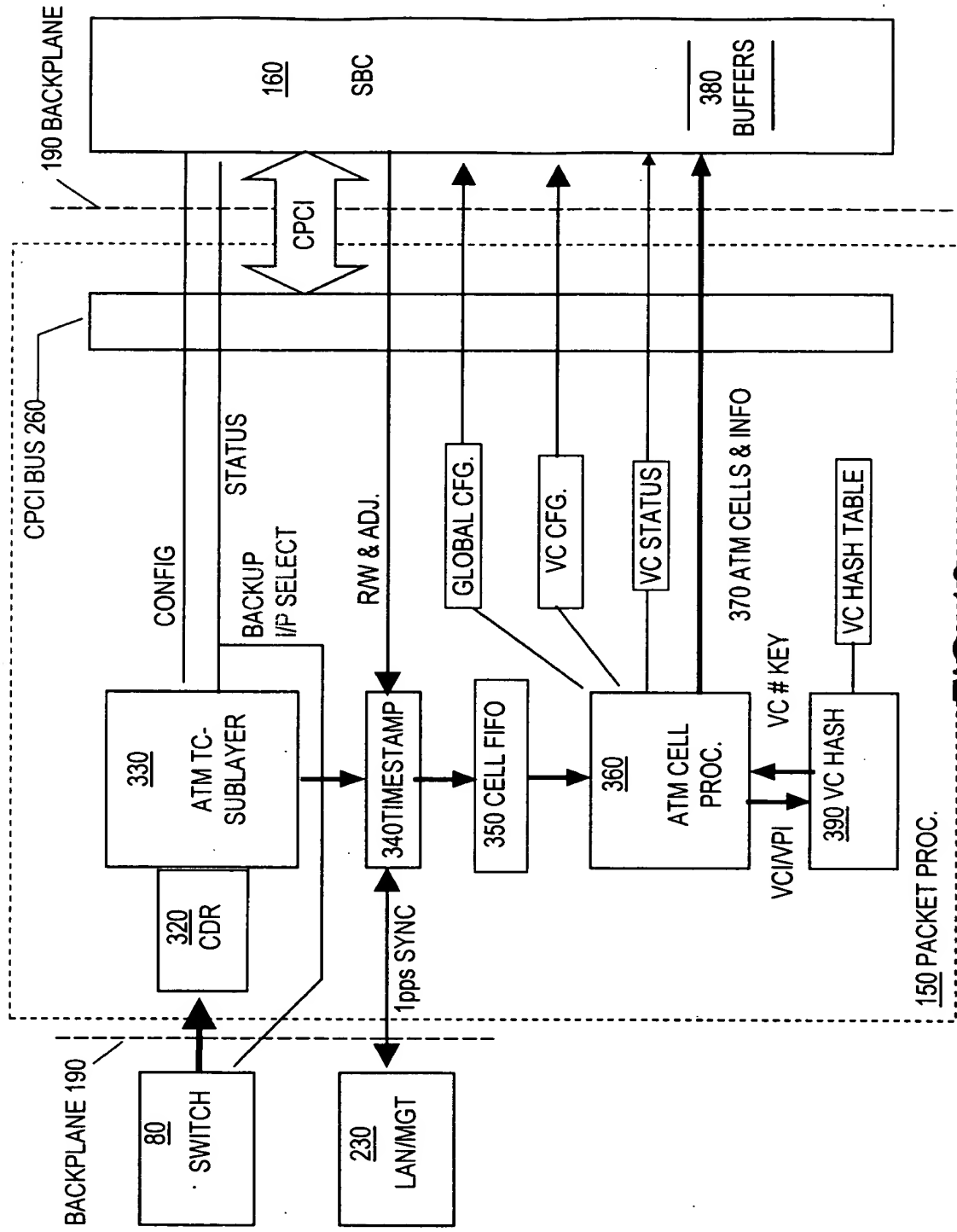


FIG. 18

